

IN THE CLAIMS:

1. (Amended) A liquid crystal display apparatus including a liquid crystal display, a thin film transistor (TFT) panel driving the liquid crystal display, and an opposing substrate, said TFT panel having a display area including a plurality of signal lines and a plurality of scanning lines [arranged intersecting with] crossing each other [and], a plurality of pixel transistors arranged [at the intersecting portions] where said signal lines and said scanning lines cross, and a driving circuit area including a plurality of driving transistors,

wherein

a gate interconnection for said driving transistors [formed] in said driving circuit area is arranged along a folded line including a first line linearly extending along a first direction, a second line linearly extending along a second direction different from said first direction, and a bent portion connecting said first and second lines; and [wherein]


said driving transistors are arranged along said first and second lines, such that channel regions of said transistors do not overlap said bent portion when viewed two-dimensionally.

2. (Amended) The liquid crystal display apparatus according to claim 1, wherein a direction from an end point closer to the display area to an end point farther from the display area of each of the lines [are] is reversed in the first and second lines, when viewed from the side of said display area.

3. (Amended) The liquid crystal display apparatus according to claim 1, wherein each of said first and second lines [consists of] includes a smaller folded line.


4. (Amended) The liquid crystal display apparatus according to claim 1, wherein said bent portion includes a line connecting said first and second lines and intersecting, almost orthogonally [with], a boundary between said display area and said driving circuit area.

5. (Amended) The liquid crystal display apparatus according to claim 1, wherein said bent portion includes a portion where said first and second lines are connected directly, [with] at an angle.

 6. (Amended) The liquid crystal display apparatus according to claim 1, wherein [direction of] a width of the channel region of said driving transistors is [arranged to be] parallel to said first and second lines.

7. (Amended) The liquid crystal display apparatus according to claim 1, wherein said display area is rectangular, and said driving circuit area [is arranged] does not [to] extend beyond a space between lines [extended] extending from two opposing [two] parallel sides of said [rectangular] display area.

8. (Amended) The liquid crystal display apparatus according to claim 1, wherein a distance between [one] a first of said driving transistors and [another] a second of said driving transistors, neighboring and positioned nearest to said [one] first driving transistor, viewed from [the side of] said display area, is longer than an interval of pitch stripes that are traces of scanning of laser beam irradiation.

 9. (Amended) The liquid crystal display apparatus according to claim 1, wherein, in the channel region of each of said driving transistors, a distance

between a corner of the channel region nearest to said display area and a corner of the channel region farthest from said display area, viewed from [the side of] said display area, is longer than an interval of pitch stripes that are traces of scanning of laser beam irradiation.

10. (Amended) A thin film transistor panel for driving a liquid crystal, having a display area including a plurality of signal lines and a plurality of scanning lines [arranged intersecting with] crossing each other and a plurality of pixel transistors arranged [at the intersecting portions] where said signal lines and said scanning lines cross, wherein

a gate interconnection for said driving transistors [formed] in said driving circuit area is arranged along a folded line including a first line linearly extending along a first direction, a second line linearly extending along a second direction different from said first direction, and a bent portion connecting said first and second lines; and [wherein]

said driving transistors are arranged along said first and second lines, such that channel regions of said transistors do not overlap said bent portion when viewed two-dimensionally.